Fig. 1 (1)

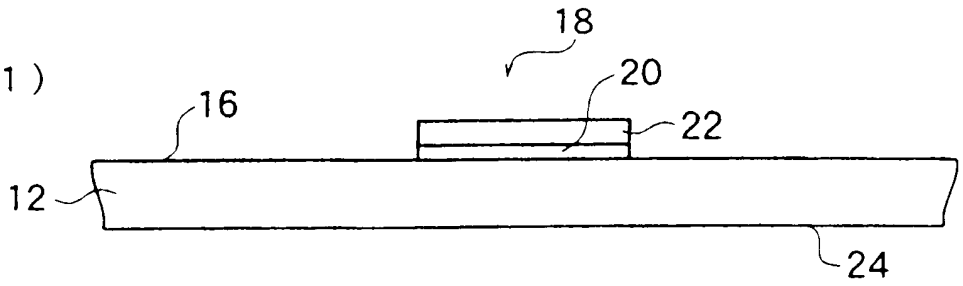


Fig. 1 (2)

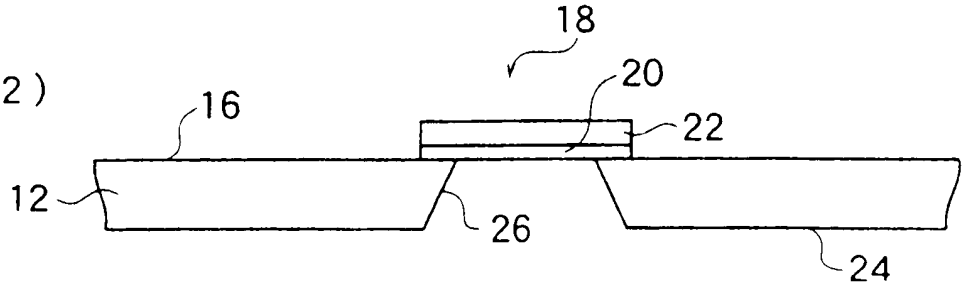


Fig. 1 (3)

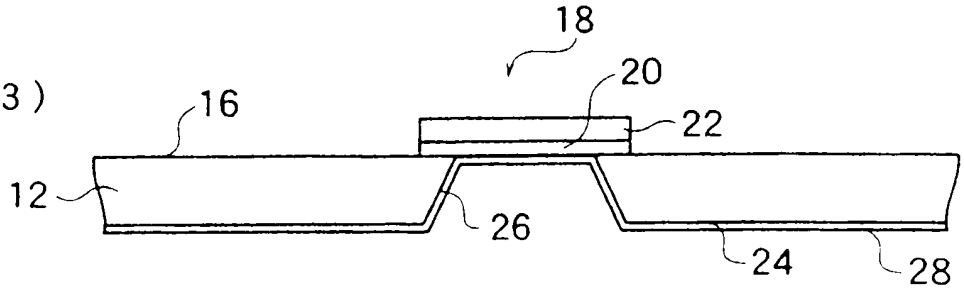


Fig. 1 (4)

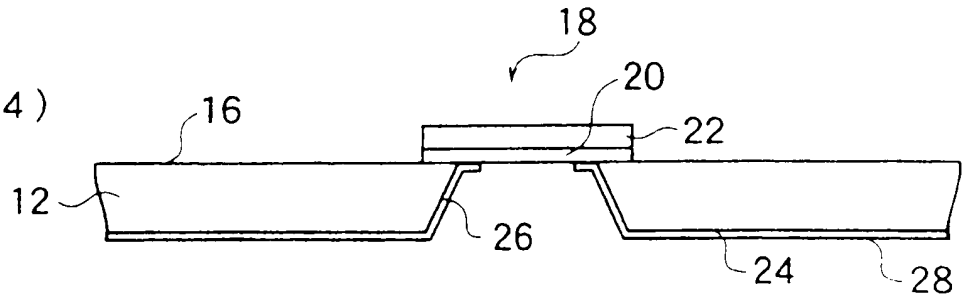
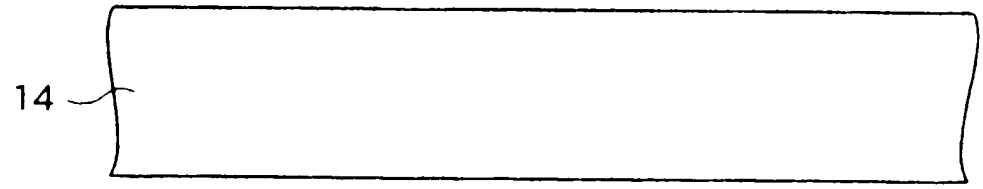


Fig. 2 (1)



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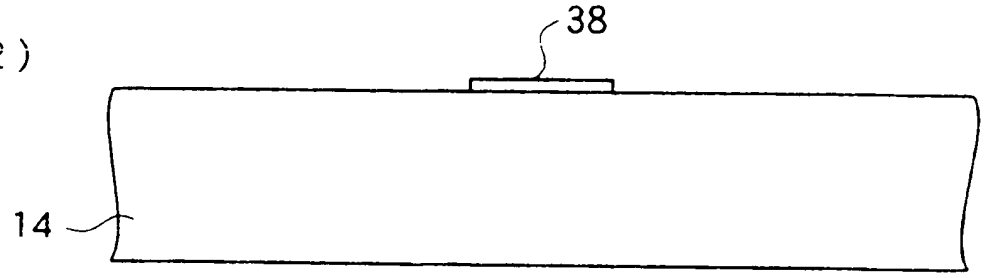

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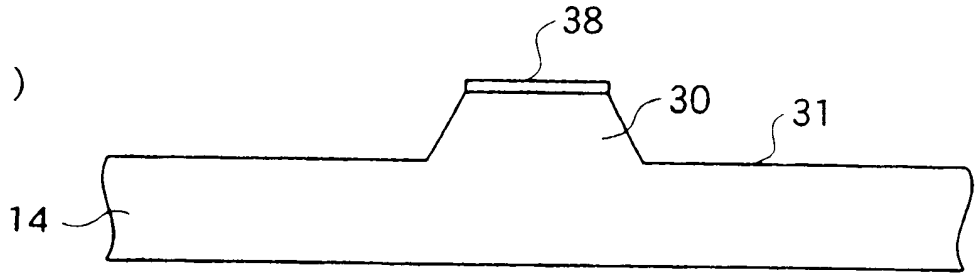

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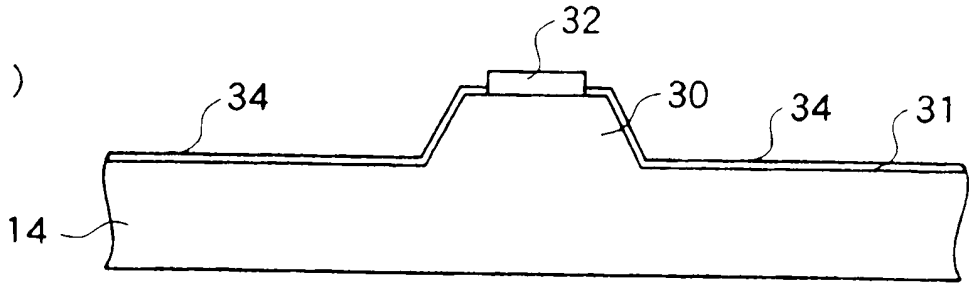


Fig. 3 (1)

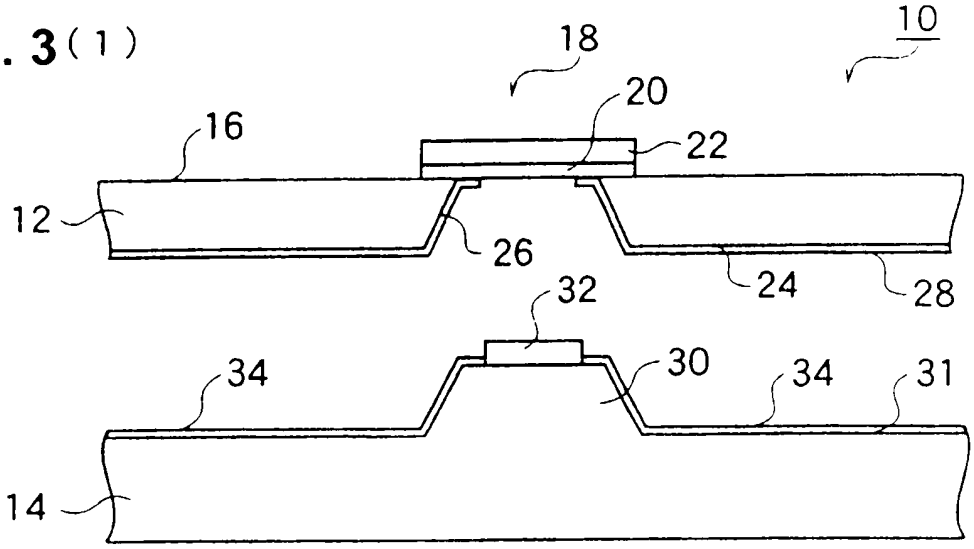
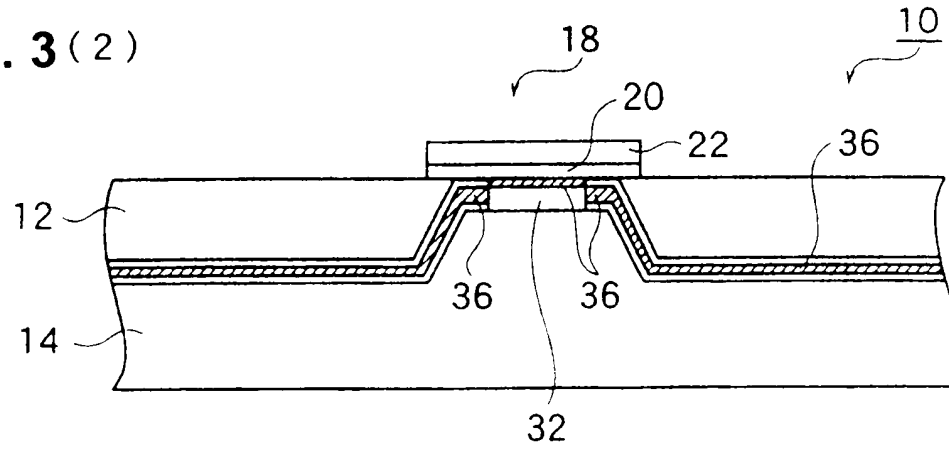
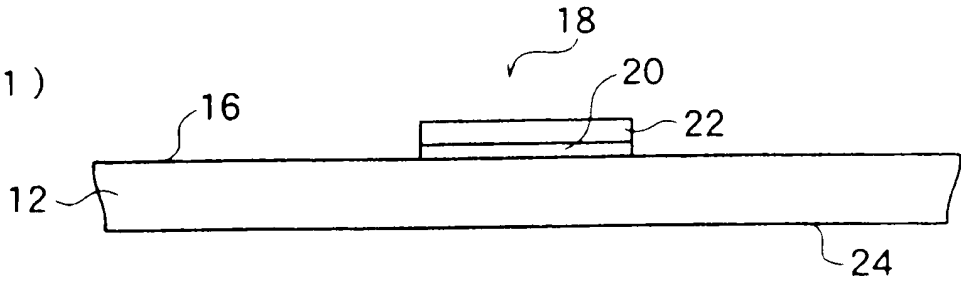


Fig. 3 (2)



10 : Semiconductor device
12 : First semiconductor chip
14 : Second semiconductor chip
16 : Surface
18 : Electrode
32 : Abutting electrode

Fig. 4 (1)



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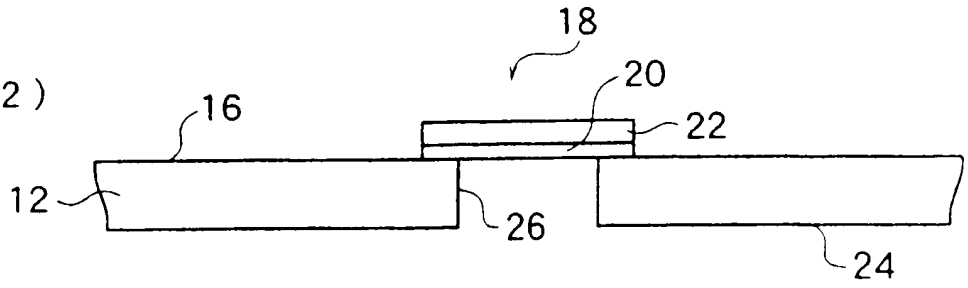

Fig. 4 (3)

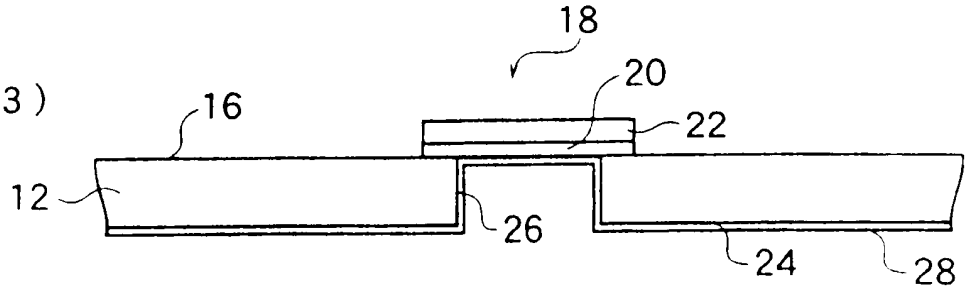

Fig. 4 (4)

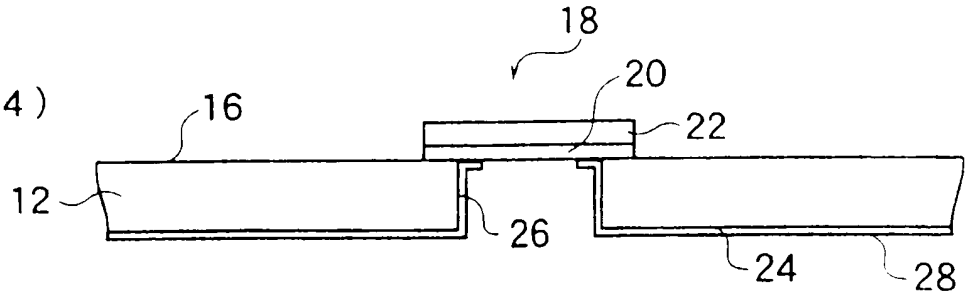

Fig. 5

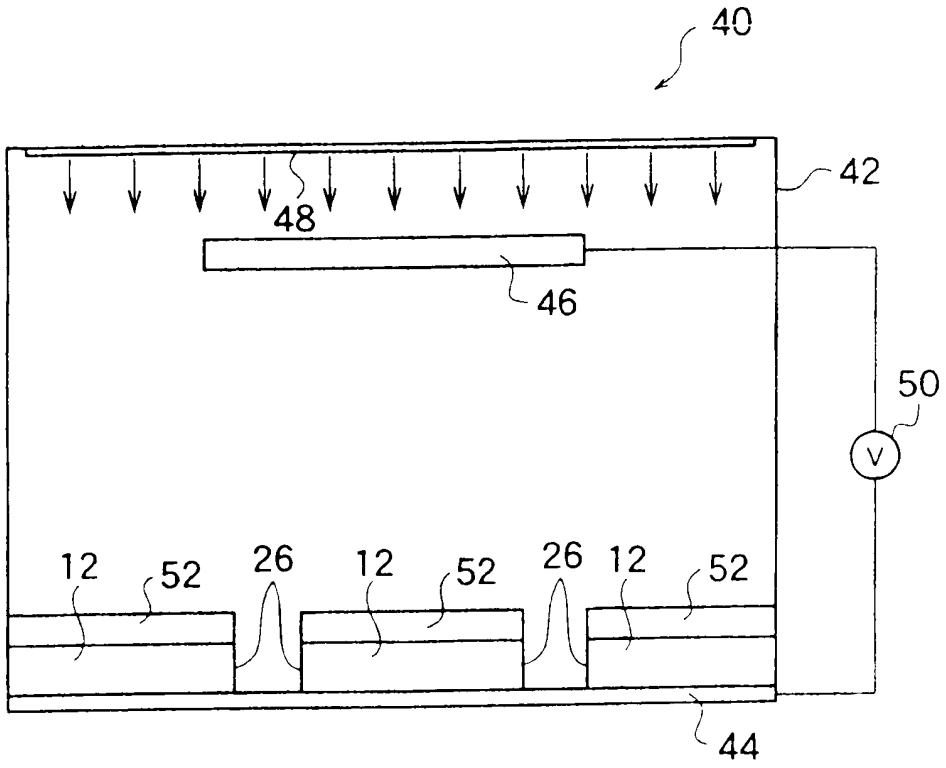

Fig. 6 (1)

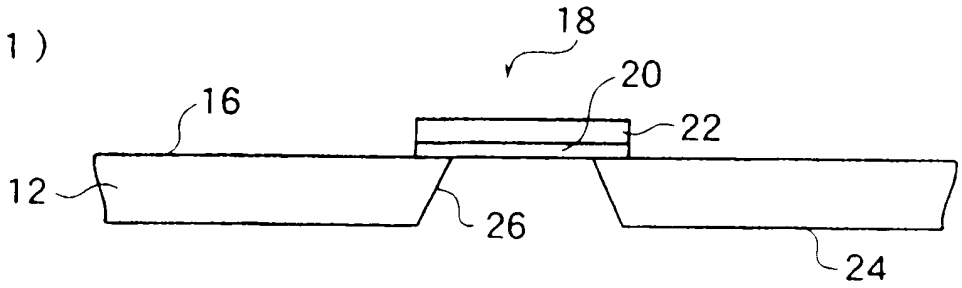

Fig. 6 (2)

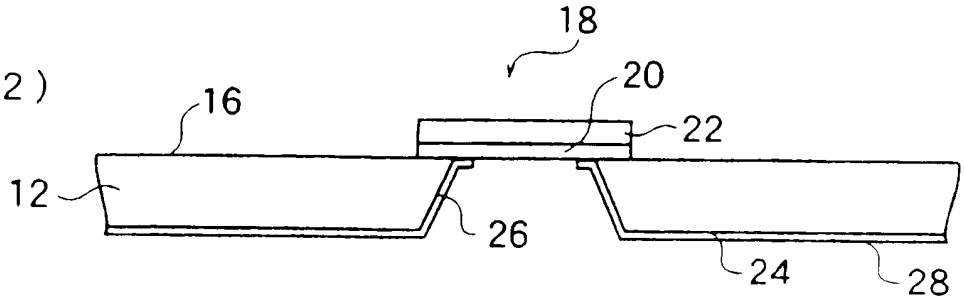

Fig. 6 (3)

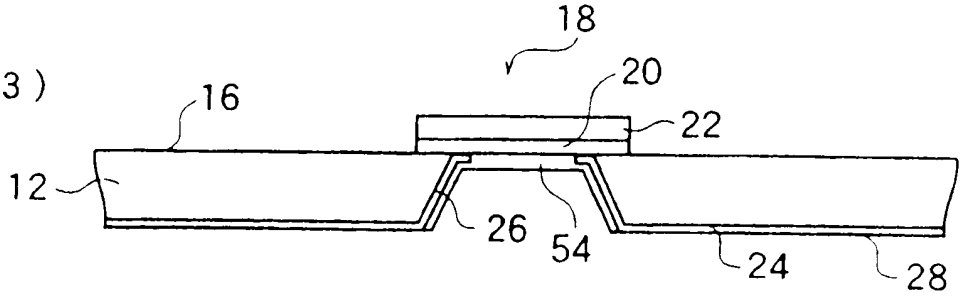

Fig. 6 (4)